



ATTORNEY DOCKET NO. 43889-937  
PATENT

6/3  
cc  
7/31/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
Sadami Takeoka et al. )  
)  
Serial No.: 09/843,687 )  
(CON of Serial No. 08/803,145) )  
)  
Filed: April 30, 2001 )  
)  
Title: METHOD OF DESIGNING SEMI- )  
CONDUCTOR INTEGRATED CIRCUIT )

Group Art Unit: 2123

Examiner: THAI PHAN

REC-RECEIVED  
JUL 27 2001  
Technology Center 2100

**PRELIMINARY AMENDMENT**

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please cancel claims 3, 4, 6, 7, 9, 10, 13, 14, and 18 without prejudice or disclaimer.

Please amend claims 1, 2, 5, 8, 11, 12, 15, 17, 19 and 20 as follows:

1. (Amended) A method of wiring a semiconductor integrated circuit to include a scan chain between first and second memory elements previously selected in the semiconductor integrated circuit, said method comprising:

B1